

Embedded FPGA Market: Global Industry Trends, Share, Size, Growth, Opportunity and Forecast 2023-2028

Market Report | 2023-10-15 | 140 pages | IMARC Group

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Report description:

Market Overview:

The global embedded FPGA market size reached US\$ 7.1 Billion in 2022. Looking forward, IMARC Group expects the market to reach US\$ 19.5 Billion by 2028, exhibiting a growth rate (CAGR) of 16.7% during 2023-2028.

Embedded field-programmable grid array (FPGA) is an intellectual property (IP) core integrated into a system on chips (SoC) or application-specific integrated circuits (ASIC) to allow post-production customization and updates. Some commonly used non-volatile and programmable technologies in embedded FPGA include electronically erasable programmable read-only memory (EERAM), anti-fuse, flash, and static random-access memory (SRAM). Embedded FPGA is widely used in hardware acceleration for machine learning (ML), defense equipment, wireless body sensor network, biomedical engineering, diagnostic imaging, electromedical segments, and genomics. As compared to traditional FPGAs, embedded FPGA is more cost-effective, energy-efficient, reliable, and offers better performance. As a result, it finds extensive applications across the consumer electronics, defense, aerospace, automotive, and telecommunication industries.

Embedded FPGA Market Trends:

The global embedded FPGA market is primarily being driven by the increasing adoption of wireless connectivity across establishments. Embedded FPGA is widely used for flexibility and real-time update of protocols and algorithms in long-term-evolution (LTE), third-generation (3G), fourth-generation (4G), and fifth-generation (5G) communication technologies. Furthermore, the increasing solution employment in data processing applications to provide fast execution and solve parallel, high throughput problems related to image and signal processing is providing a considerable boost to market growth. Additionally, the widespread product adoption to develop next-generation artificial intelligence (AI) accelerators with low data latency, high compatibility, and reduced costs is providing an impetus to the market growth. Moreover, the increasing utilization of the internet of things (IoT)-enabled devices to provide enhanced security and allow user-defined customization is creating a positive outlook

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for the market. Other factors, including the rising need to enhance the safety and operational range of military communication systems and increasing product demand from the industrial sector for regulating voltage, current, and motor speed, are anticipated to drive the market toward growth.

Key Market Segmentation:

IMARC Group provides an analysis of the key trends in each sub-segment of the global embedded FPGA market report, along with forecasts at the global, regional and country level from 2023-2028. Our report has categorized the market based on technology and application.

Breakup by Technology:

EEPROM

Antifuse

SRAM

Flash

Others

Breakup by Application:

Data Processing

Consumer Electronics

Industrial

Military and Aerospace

Automotive

Telecom

Others

Breakup by Region:

North America

United States

Canada

Asia-Pacific

China

Japan

India

South Korea

Australia

Indonesia

Others

Europe

Germany

France

United Kingdom

Italy

Spain

Russia

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Others
Latin America
Brazil
Mexico
Others
Middle East and Africa

Competitive Landscape:

The competitive landscape of the industry has also been examined along with the profiles of the key players being Achronix Semiconductor Corporation, Adicsys, Advanced Micro Devices Inc, Efinix Inc, Flex Logix Technologies Inc., Intel Corporation, Lattice Semiconductor Corporation, Menta S.A.S, Microchip Technology Inc and Quick Logic Corporation.

Key Questions Answered in This Report:

How has the global embedded FPGA market performed so far and how will it perform in the coming years?

What has been the impact of COVID-19 on the global embedded FPGA market?

What are the key regional markets?

What is the breakup of the market based on the technology?

What is the breakup of the market based on the application?

What are the various stages in the value chain of the industry?

What are the key driving factors and challenges in the industry?

What is the structure of the global embedded FPGA market and who are the key players?

What is the degree of competition in the industry?

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